COMPLETED PROJECTS					
Sr. No	Title of the project	Sponsoring agency	Date	Total Sanctioned Amount	Coordinator / Project Investigator
1	Laboratory Development for Wireless Communication	Agilent Technologies	28/03/2011 5 years	16 lacs in terms of System Vue Software and VSA	Dr. U. D. Dalal
2	Spectrum Security in Cognitive Radio.net	University of Pune, BCUD	January, 2012 2 Years		Dr. V. Mishra
3	Characterization of Porous Silicon Structure	INUP, IIT Bombay	January, 2012 6 months		Dr. V. Mishra ⁄Ir. P. N. Patel A. K. Panchal
4	Development of Intelligent optical bio-chemical sensor using optical fiber gratings	DST	01/10/2012 3 Years	Rs. 27,60,000.00	Dr. V. Mishra
5	Fabrication of Fiber Granting	INUP, IIT Bombay	January, 2013 6 months		Dr. V. Mishra
6	Special Manpower Development Project for VLSI Design and Related Software (SMDP-II)	DIT, MCIT, Gol, New Delhi	08-11-2005 to 31-05-2013 5 Years	Rs. 45,47,950.00	Mr. A. D. Darji Mr. Z. M. Patel
7	Fabrication & Characterization of Conventional & Quantum Dots Floating GATE MOSCAP Structure for Thin Tunnel Oxides	INUP, IIT Bombay	1st Oct, 2013 6 months		Dr. R. M. Patrikar Dr. V. Mishra Ms. R. N. Dhavse
8	Algorithm Development for Analysis of Biochemical compound using Optical Probing Technique	TEQIP-II, SVNIT, Surat	Jan-July, 2014 6 months	Rs. 50,000/-	Dr. V. Mishra P. N. Patel
9	Development of Porous Silicon based Optical Sensor for the Detection of Target Molecule in Analyte	TEQIP-II, SVNIT, Surat	Jan-July 2014 6 months	Rs. 50,000/-	V. Mishra P. N. Patel
10	Performance Appraisal of A FGMOS Transistor by Using Quantum Dots Floating Gate in 45 nm Regime	DST	21-11-2011 to 25-06-2014 2 Years	Total sanctioned Rs. 16,43,117.00 Released Rs. 15,23,117.00	Dr. V. Mishra Ms. R. N. Dhavse
11	IRNSS Navigation Receiver Field Trail and Data Collection	Space Application Center, Ahmedabad	March, 2015 2 Years		Dr. U. D. Dalal
12	Fabrication and Characterization of Porous Silicon and its Application as an Optical Biosensor	DRDO	15-03-2011to 05-05-2015 (extended 1 year)3 Years	Total sanctioned Rs. 30,00,000.00 Released Rs. 28,50,000.00	Dr. V. Mishra Mr. P. N. Patel Dr. J. N. Sarvaiya
13	FPGA Based Adaptive Filter Algorithm Implementation for external noise cancellation	DAE, BRNS, Gol, Mumbai	27/10/2015	Rs. 17,80,750.00	Dr. A. D. Darji Dr. J. N. Sarvaiya